

TPZ773PN Digital Library V210

I/O Cells

Cell Name	Description
PCIZ	TTL PCI Buffer, 5V-Tolerant
PDBxTZ	TTL 3-State Output Pad with Input, 5V-Tolerant
PDDZ	CMOS Input Pad with Pulldown, 5V-Tolerant
PDDxTZ	TTL 3-State Output Pad with Input and Pulldown, 5V-Tolerant
PDISZ	CMOS Schmitt Trigger Input Pad, 5V-Tolerant
PDIZ	COMS Input Pad, 5V-Tolerant
PDTxTZ	TTL 3-State Output Pad, 5V-Tolerant
PDUZ	COMS Input Pad with Pullup, 5V-Tolerant
PDUxTZ	TTL 3-State Output Pad with Input and Pullup, 5V-Tolerant
PDXOExAM	Crystal Oscillator with High Enable
PDXOExM	Crystal Oscillator with High Enable
PDXOxAM	Crystal Oscillator
PDXOxM	Crystal Oscillator
PRBxTZ	TTL 3-State Output Pad with Input and Limited Slew Rate, 5V-Tolerant
PRDxTZ	TTL 3-State Output Pad with Input, Pulldown and Limited Slew Rae, 5V-Tolerant
PRTxTZ	TTL 3-State Output Pad and Limited Slew Rate, 5V-Tolerant
PRUxTZ	TTL 3-State Output Pad with Input, Pullup and Limited Slew Rate, 5V-Tolerant
PVDDxZ	Vdd Pad
PVSSxZ	Vss Pad