

Technology and Specifications							Silicon Performance			
Process(nm)	IP Category	Architecture	RES (bits)	SPEED (MHz)	Power (mW)	SIZE (mm2)	SNR	THD	SNDR	ENOB
16	ADC	SAR	14	5	1	0.08	76	-80	74	12.0
16	ADC	SAR	9	80	5	0.06	52	-59	51	8.2
16	DAC	Current Steering	11	480	10	0.06	64	-68	62	10.0
22	ADC	Flash	6	10000	250	0.6	34	41	33	5.4
22	DAC	Current Steering	12	6000	20	0.4	64	-70	63	10.2
28	Video AFE	VGA+FILter+ADC	10	165	55	1.5	52	-60	51	8.2
28	ADC	Pipelined	10	165	15	0.08	52	-58	51	8.2
28	ADC	Pipelined	14	400	250	0.5	70	-75	68	11.0
40	ADC	Delta Sigma	16	0.5	2	0.15	82	-88	80	13.0
40	DAC	Current Steering	12	1000	400	3	65	-70	63	10.2
40	DAC	Delta Sigma	14	10	20	0.9	76	-80	74	12.0
40	ADC	SAR	12	200	40	0.6	65	-70	64	10.3
65	ADC	SAR	12	5	9	0.1	69	-75	68	11.0
65	IQ_ADC	Pipelined	12	80	80	0.45	65	-71	63	10.2
65	IQ_ADC	Current Steering	12	320	320	0.23	67	-72	65	10.5
65	Video AFE	VGA+FILter+ADC	10	165	165	0.2	55	-59	54	8.7
16	ADC	TI-SAR	8	7000	7000	0.3	45	-50	44	7.1
22	DAC	Current Steering	12	5000	5000	0.4	60	-55	55	8.8
90	Video DAC	Current Steering	10	300	300	0.3	56	-55	53	8.5
180	Filter+IQ_ADC	Pipelined	12	50	50	0.6	65	-70	64	10.3
180	Filter+IQ_ADC	Current Steering	12	160	160	0.25	67	-71	65	10.5
40	IQ_ADC	Pipelined	12	160	160	0.33	65	-68	63	10.2
40	IQ_DAC	Current Steering	12	640	640	0.21	66	-71	65	10.5
40	IQ_ADC	Pipelined	12	120	120	0.33	65	-68	64	10.4
40	IQ_DAC	Current Steering	12	320	320	0.21	66	-72	65	10.4
350	ADC	Pipelined	8	20	20	1.7	48	53	47	7.5
16	DACX3	Current Steering	10	320	320	0.2	62	-60	53	8.5
40	8 channel ADC	Pipelined	14	125	125	0.33	70	-71	68	11.0
40	IQ_ADC	Current Steering	14	500	500	1.3	70	-72	68	11.0
55	ADC	SAR	12	5	5	0.22	73	-76	71	11.5
40	DAC_signle end	Current Steering	12	200	200	0.06	62	-54	53	8.5
110	ADC	Pipelined	10	165	165	0.21	55	-65	56	9.0
130	DAC	R2R	12	5	5	0.2	70	-76	71	11.5
12	DAC	Current Steering	12	5000	5000	0.4	64	-66	62	10.0